

FIG. 1A

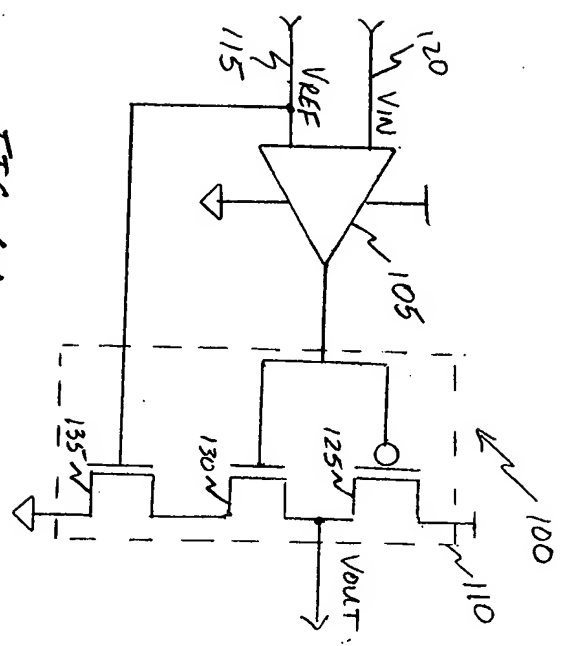
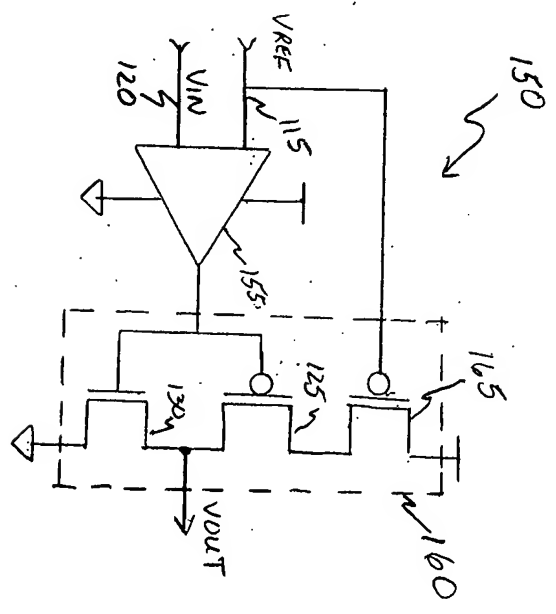
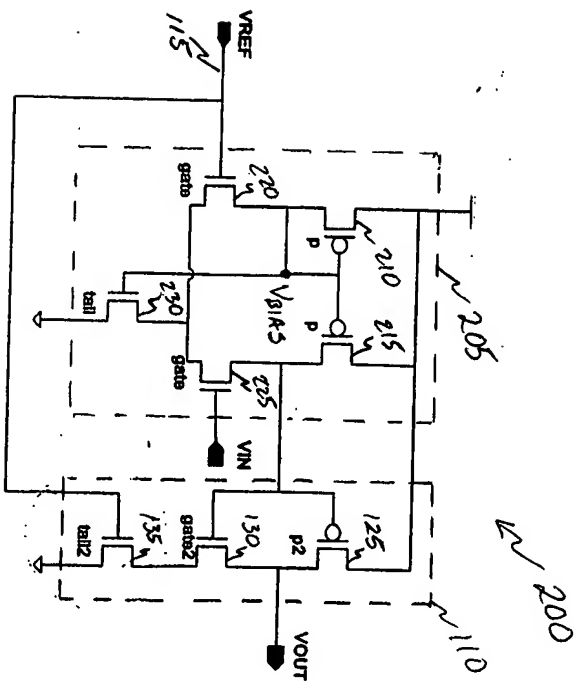


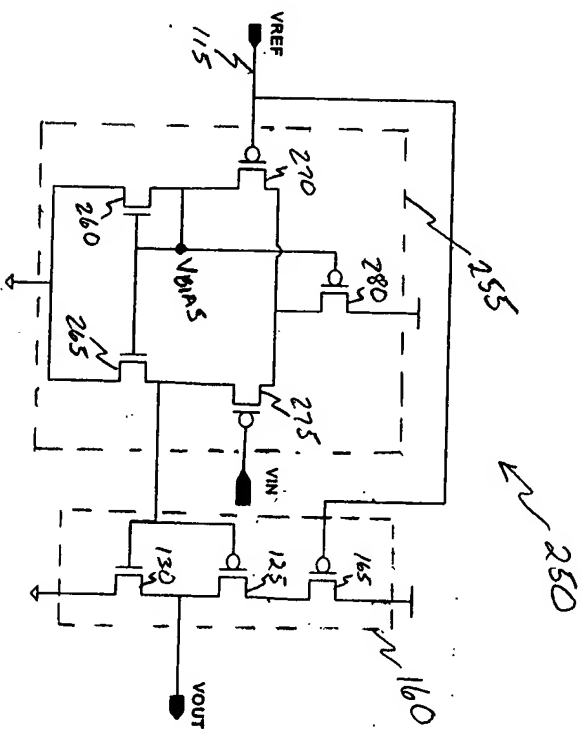
FIG. 1B





REFERENCE VOLTAGE MODULATED VERSION  
OF THE NMOS SELF-BIASED DIFF AMP

FIG. 24



REFERENCE VOLTAGE MODULATED VERSION  
OF THE PMOS SELF-BIASED DIFF AMP

FIG. 28

300

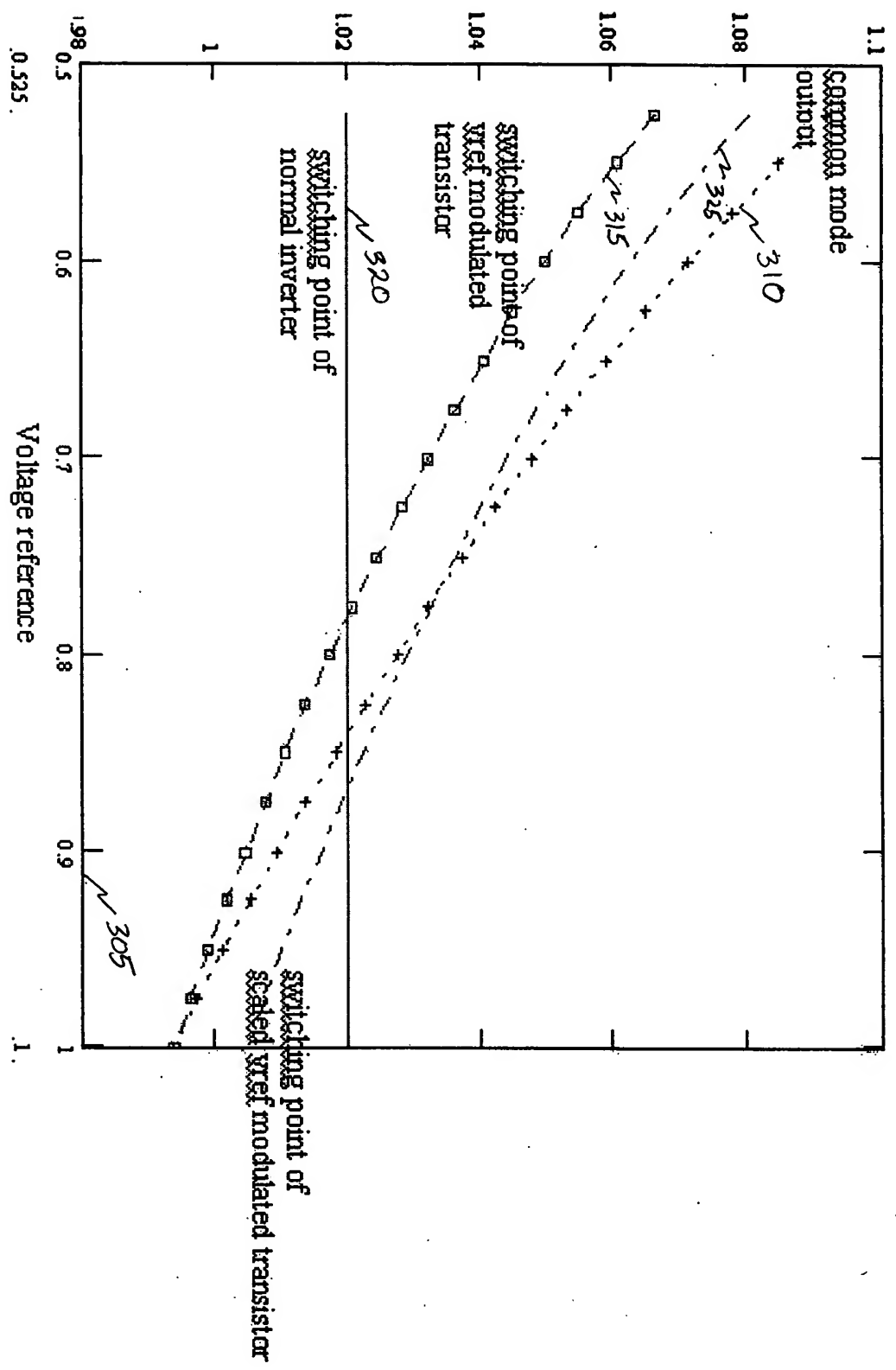


FIG. 3

SCALED REFERENCE VOLTAGE MODULATED  
VERSION OF THE NMOS SELF-BIASED DIFF AMP

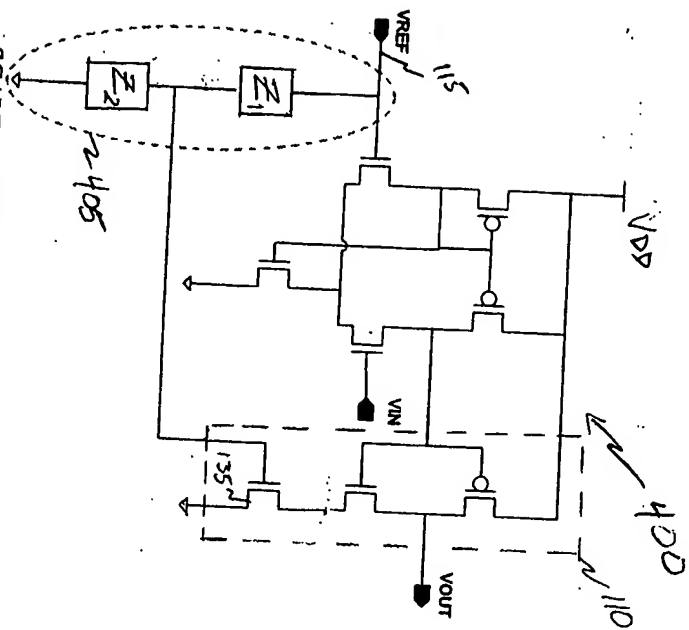


FIG. 4A

SCALED REFERENCE VOLTAGE MODULATED  
VERSION OF THE PMOS SELF-BIASED DIFF AMP

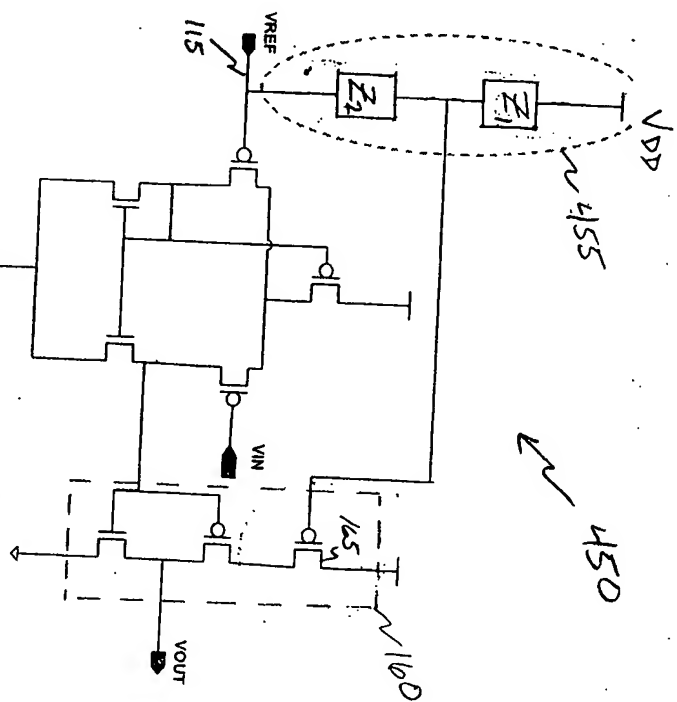


FIG. 4B

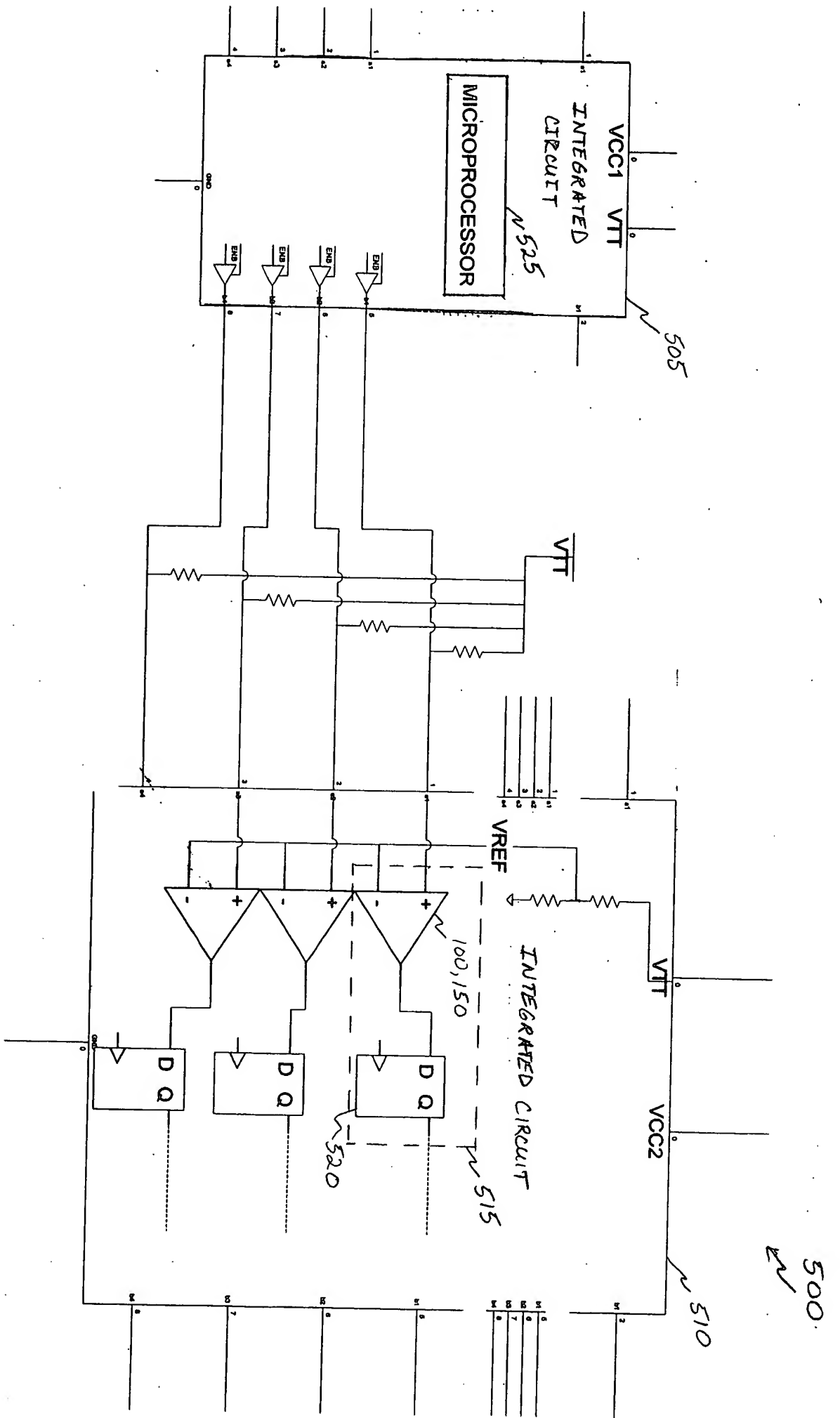


FIG. 5

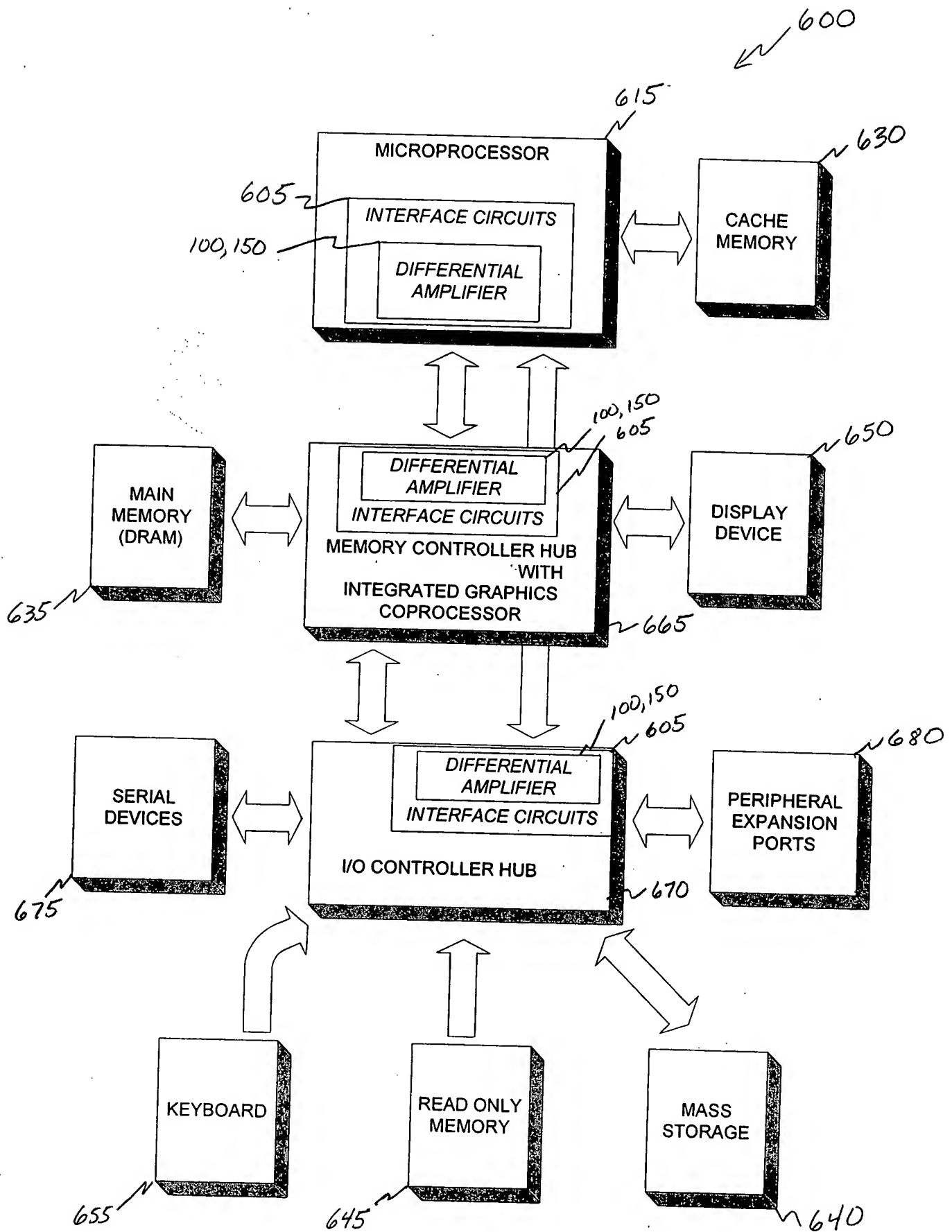


FIG. 6

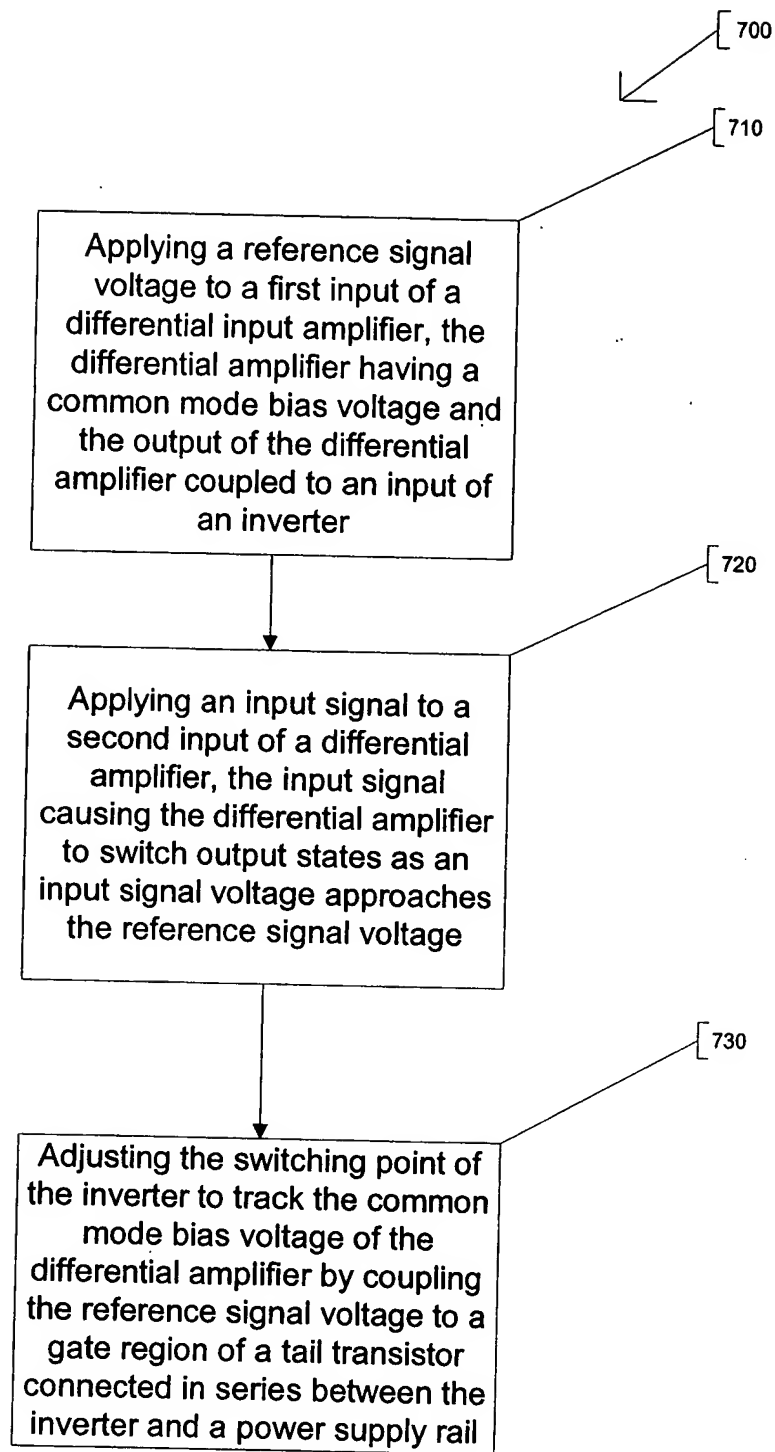


FIG. 7